
Release Notes

Incremental update of Processor Expert Software for i.MX Processors v1.x i.MX 6UltraLite Service Pack

Product Content

- CPUs – iMX 6UltraLite in 14x14 package
 - MCIMX6G3DVM05AA
 - MCIMX6G2DVM05AA
 - MCIMX6G1DVM05AA
 - MCIMX6G0DVM05AA
- CPUs – iMX 6 UltraLite in 9x9 package
 - MCIMX6G3DVK05AA
 - MCIMX6G2DVK05AA
- Boards supported
 - BGA EVK based on the i.MX 6UltraLite
- PinMux configuration data for a PinSettings component based on the following sources:
 - iMX6UL_RM_Rev_0.pdf Rev 0, 08/2015
 - iMX6ULCEC_Rev0.pdf Rev 0, 08/2015
- **Known problems and limitations**
 - None.
- **Revision history**
 - Updated part number identification using fully qualified part name.
 - Updated feature support for “G0” and “G1” devices – fixed UART, ENET, USB, LCDIF, QSPI, SDIO, ISO7816-3, SPI, Timers and ADC peripheral signals.
 - Support of device changes based on the last datasheet and RM Rev.0, 08/2015.
 - Fixed initialization of ‘SELECT_INPUT’ registers involved in daisy chaining of some input pad signals.

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